


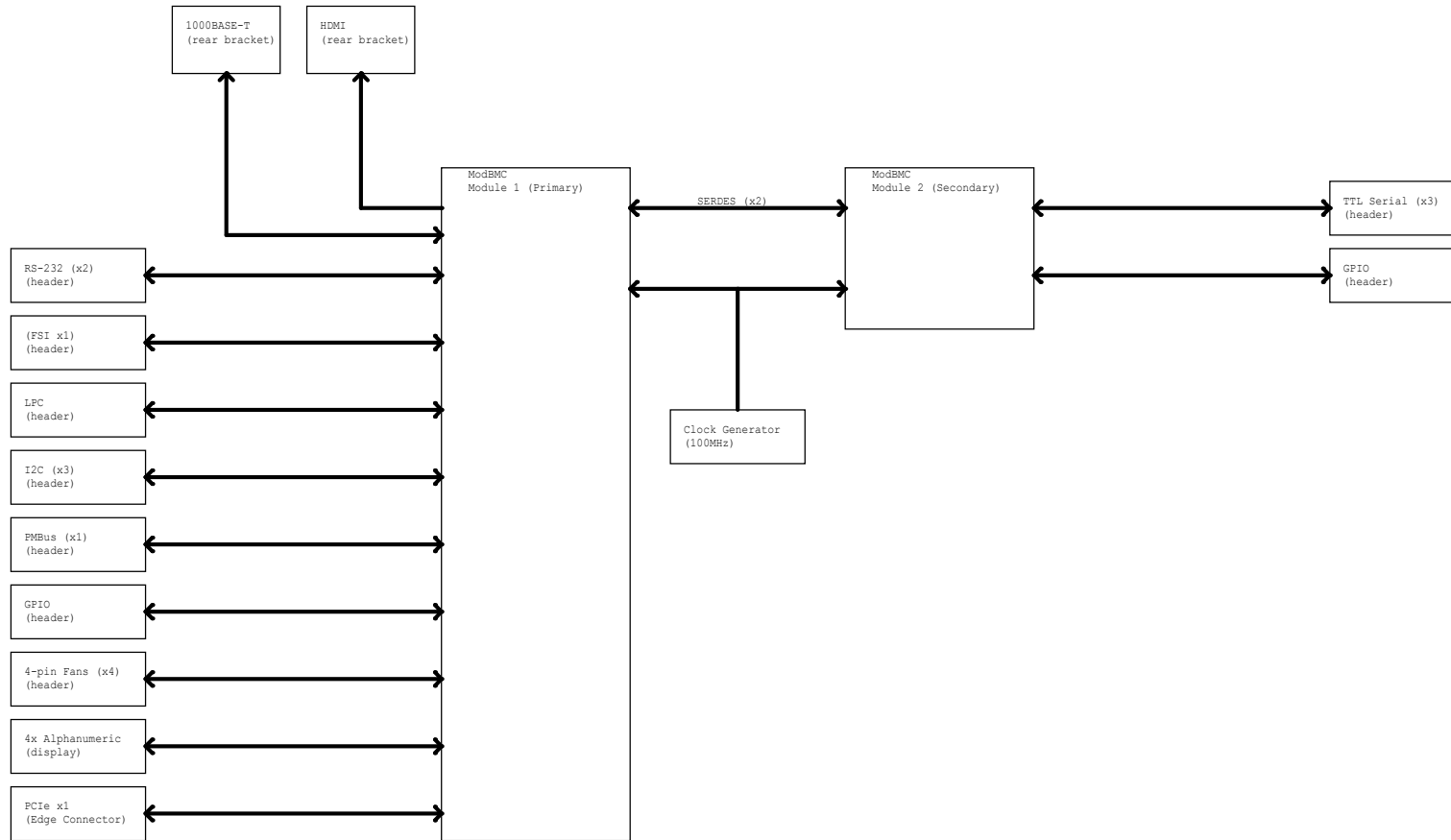
1	Table of Contents
2	Block Diagram
3	Clock Distribution
4	Blank
5	Power Distribution
6	PCIe / Power
7	Blank
8	FSI / PMBus
9	Inter-Card SERDES / Clock
10	Module Power / Control
11	HDMI Connectors
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13	Card 2 COM Ports
14	GPIO
15	Module SPI
16	Module LPC
17	Module Fan / Tach
18	LED Display
19	Ethernet
20	Module I2C
21	Blank
22	Blank
23	Fan Connectors
24	Firmware ROMs
25	Blank
26	Blank
27	Programming Headers

PCB1


 PCB-SILVERTERN-099

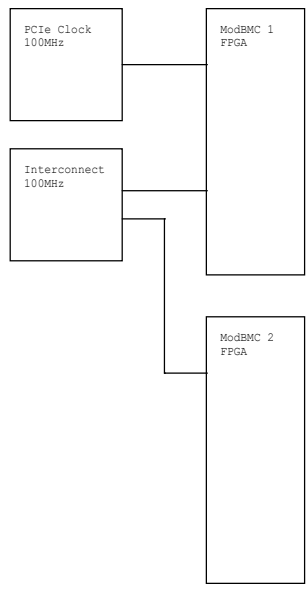
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Project :	Arctic Tern PCIe Carrier	
Part NO :	AT1PC1	
EC NO :	1.00	
Title :	Table of Contents	
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Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	Block Diagram		
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Project :	Arctic Tern PCIe Carrier	
Part NO :	AT1PC1	
Title :	Clock Distribution	
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Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
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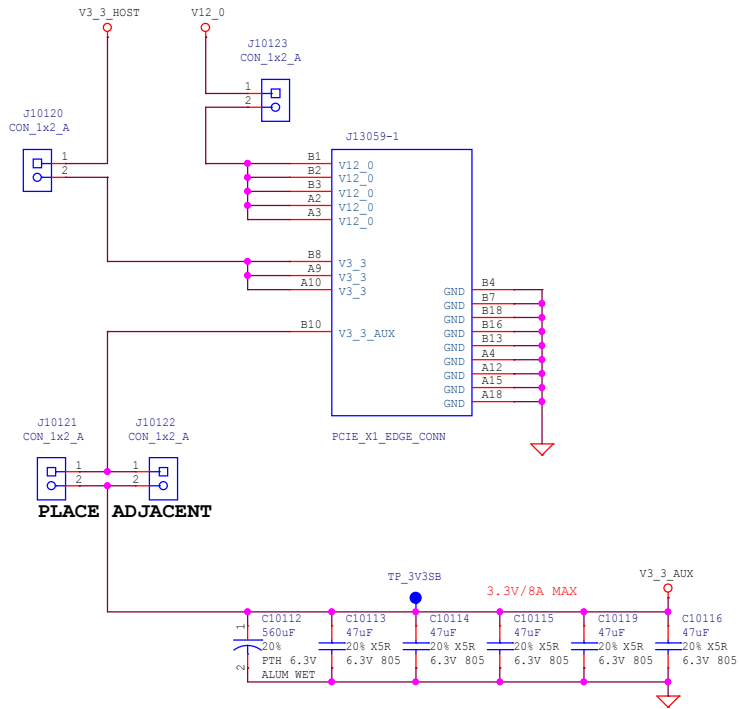
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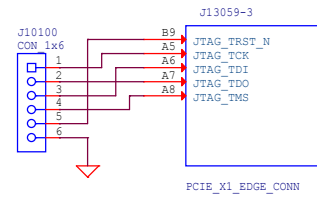
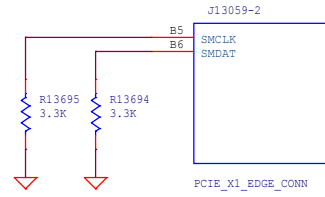


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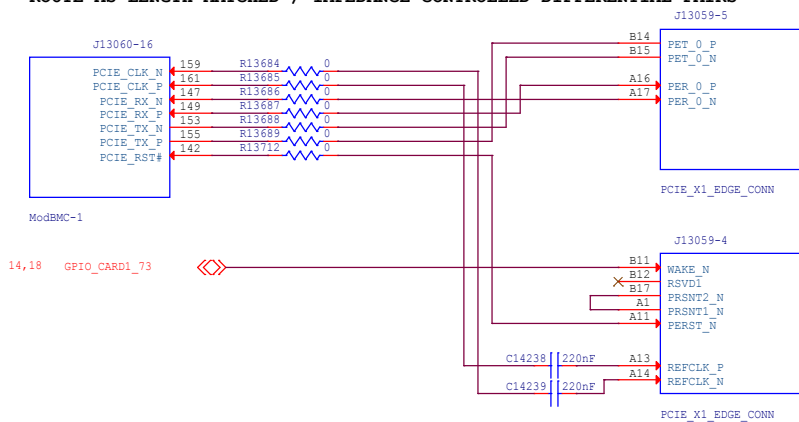
Project :	Arctic Tern PCIe Carrier	
Part NO :	AT1PC1	
Title :	Power Distribution	
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PLACE ADJACENT



ROUTE AS LENGTH MATCHED / IMPEDANCE CONTROLLED DIFFERENTIAL PAIRS



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Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : PCIe / Power		
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Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : Blank		
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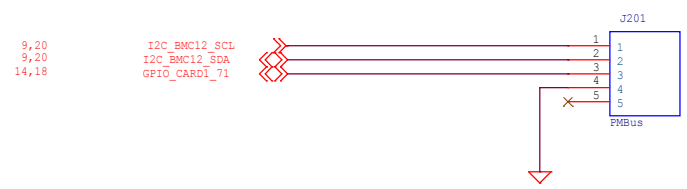
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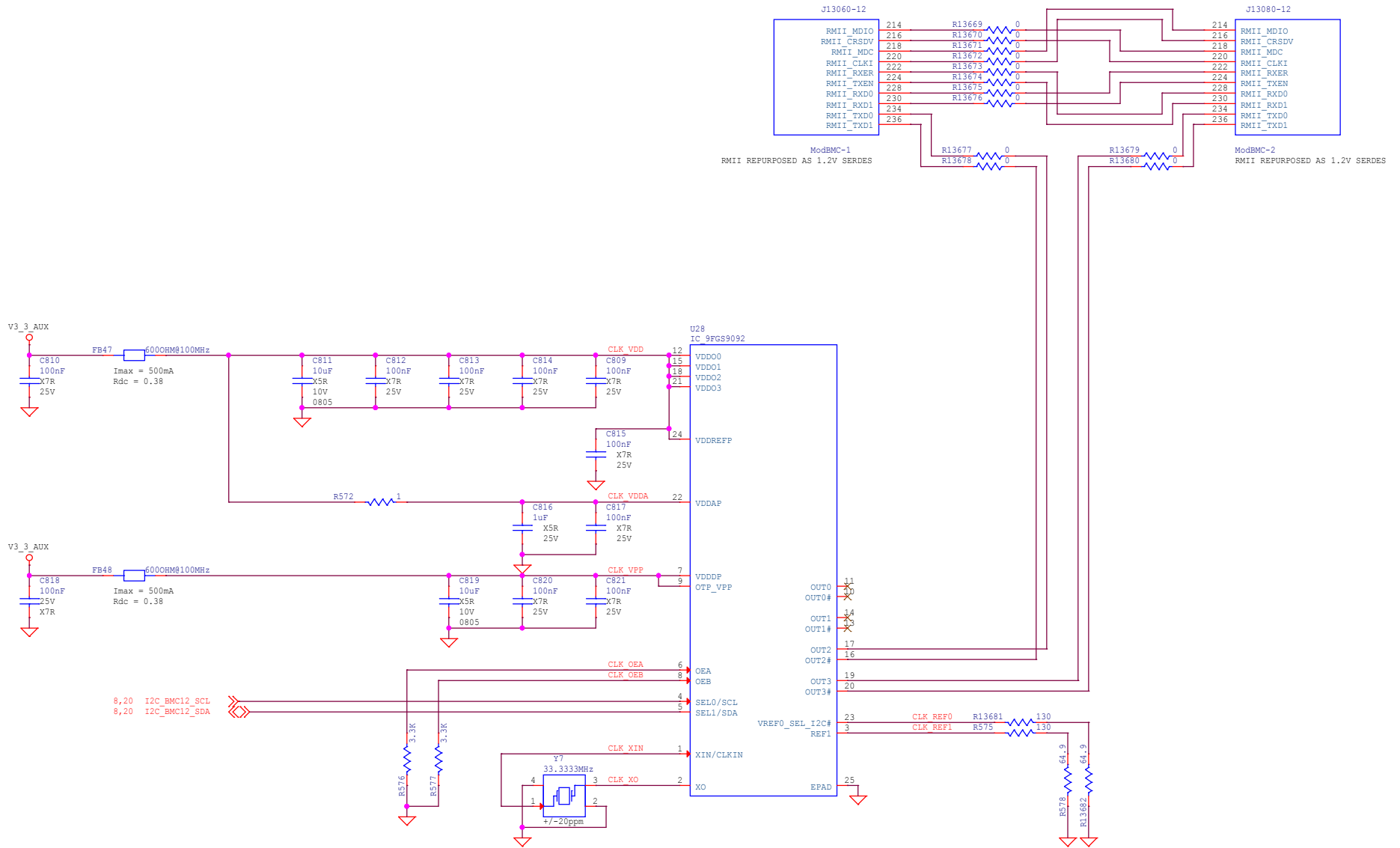
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Project :	Arctic Tern PCIe Carrier	
Part NO :	AT1PC1	
Title :	FSI / PMBus	
Author :		Page: 8 of 27
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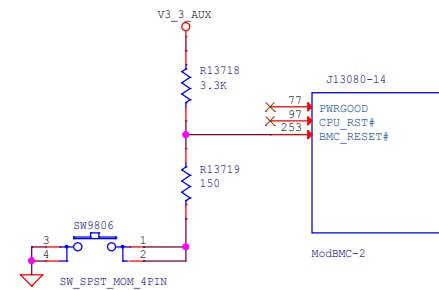
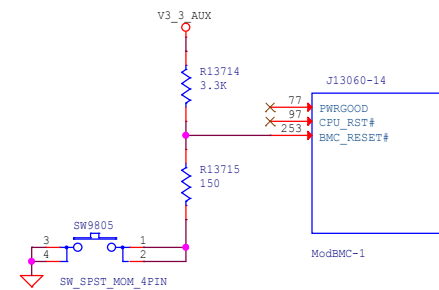
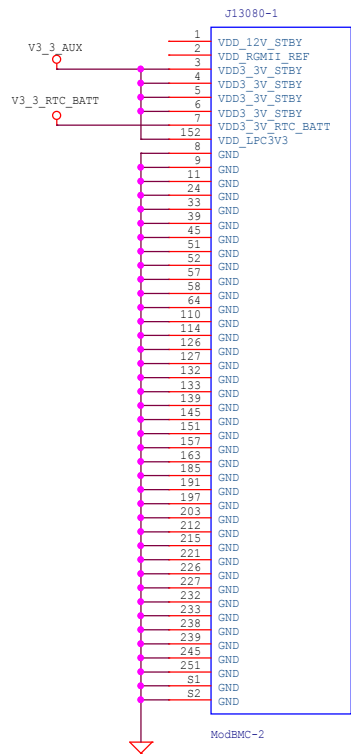
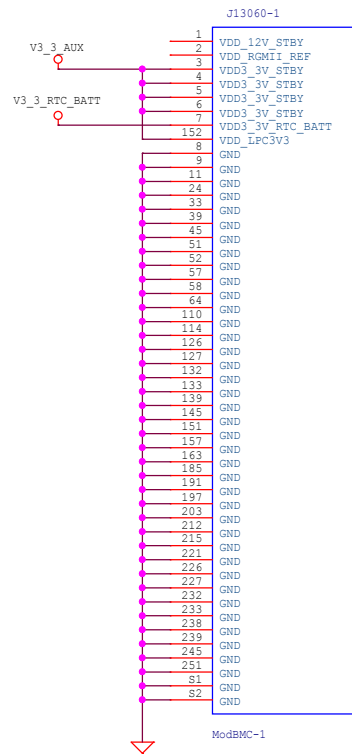
ROUTE ALL AS LENGTH MATCHED / IMPEDANCE CONTROLLED DIFFERENTIAL PAIRS



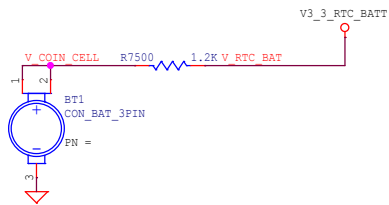
Keep [00] to leave CPU 0 clock running
 Disconnect OEA/B from FPGA. No need as 00 is CPU0 running. Needed 11 to have both CPU0 and CPU1 clocks running.

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Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	Inter-Card SERDES / Clock		
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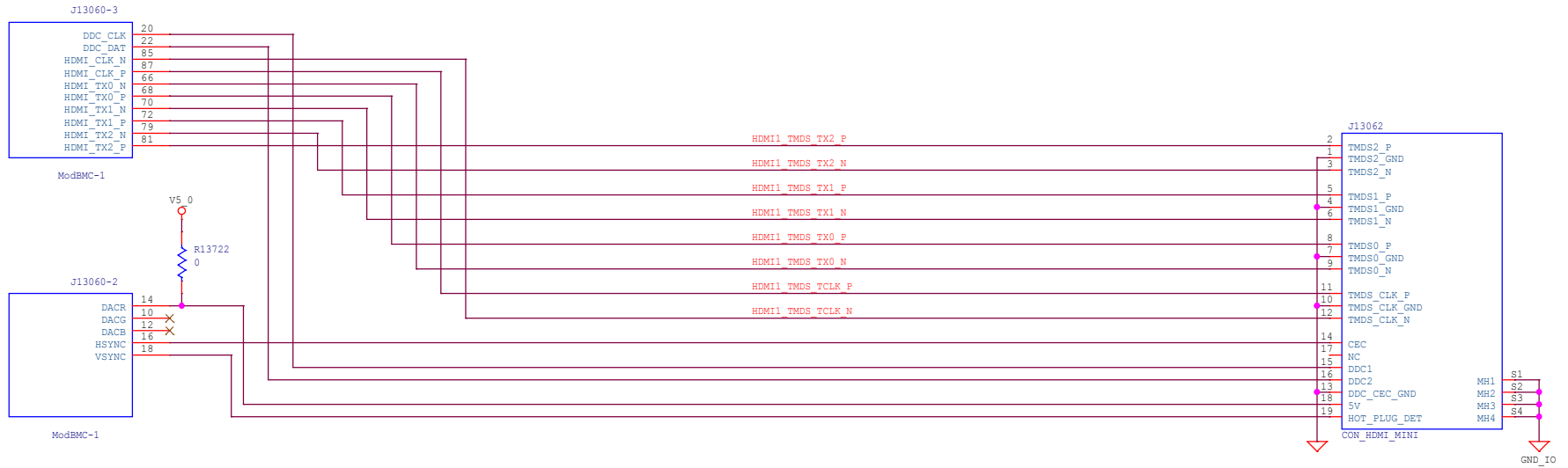
XBT7500 377/376
BATTERY



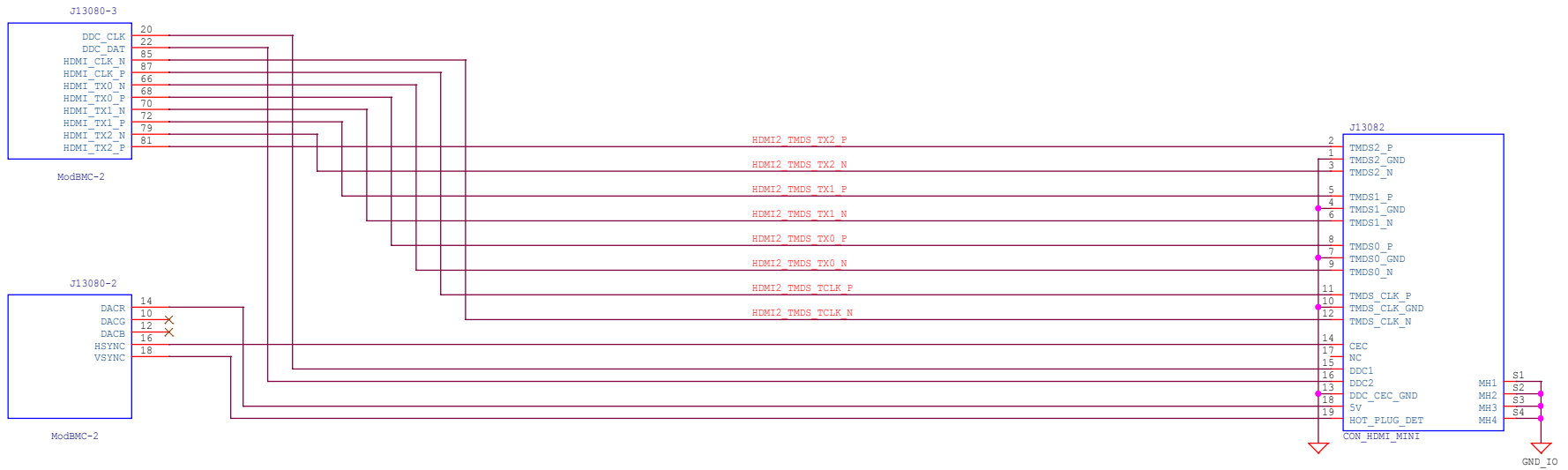
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Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	Module Power / Control		
Author :			Page:10 of 27
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HDMI Connector (ModBMC Card 1)

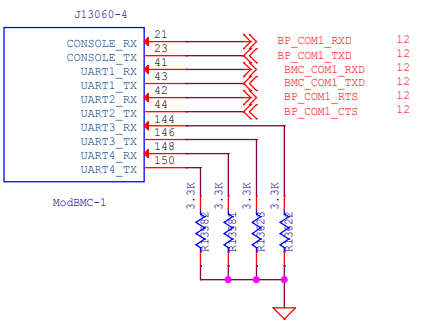
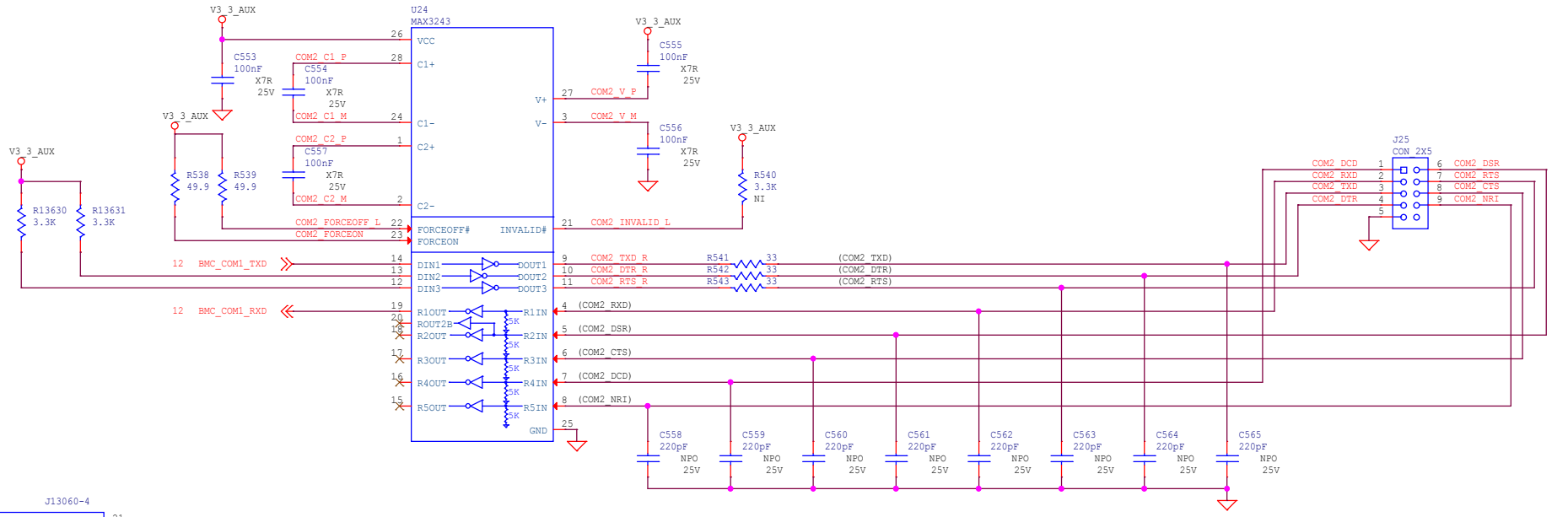
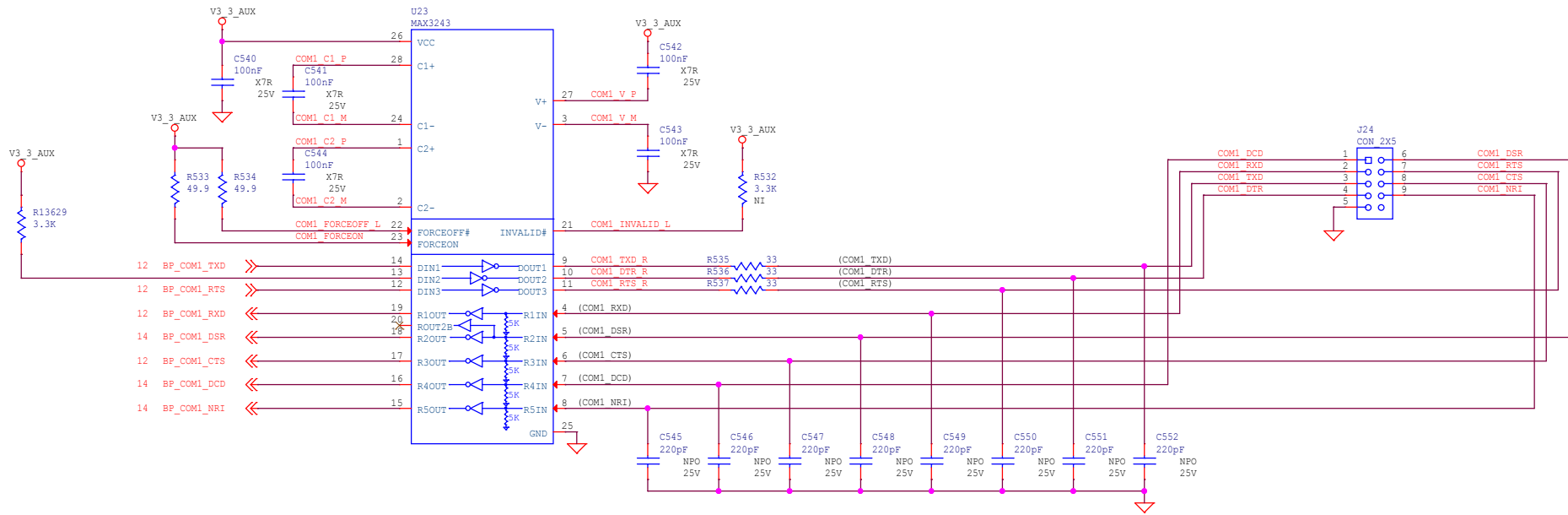


HDMI Connector (ModBMC Card 2)




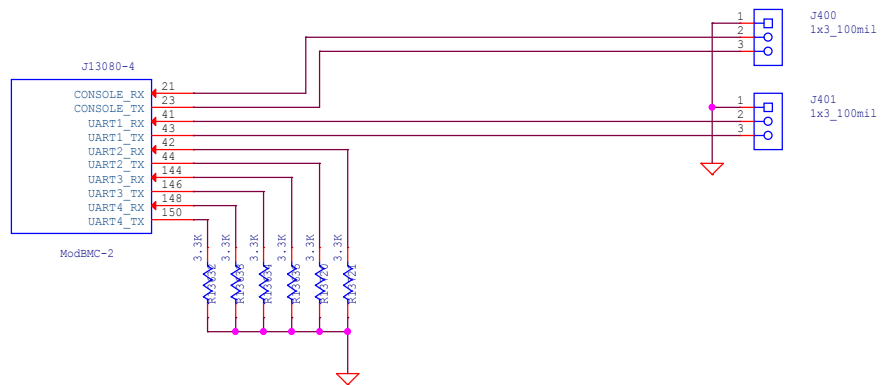
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Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	HDMI Connectors		
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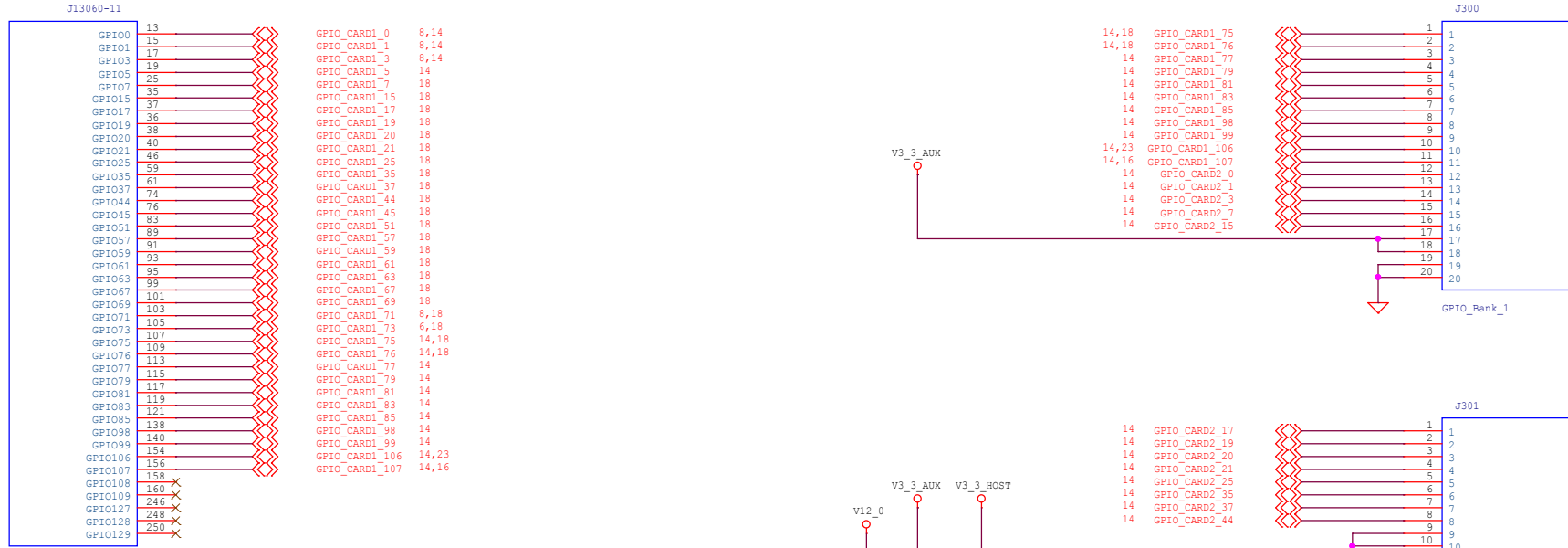
Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : Card 1 COM Ports		
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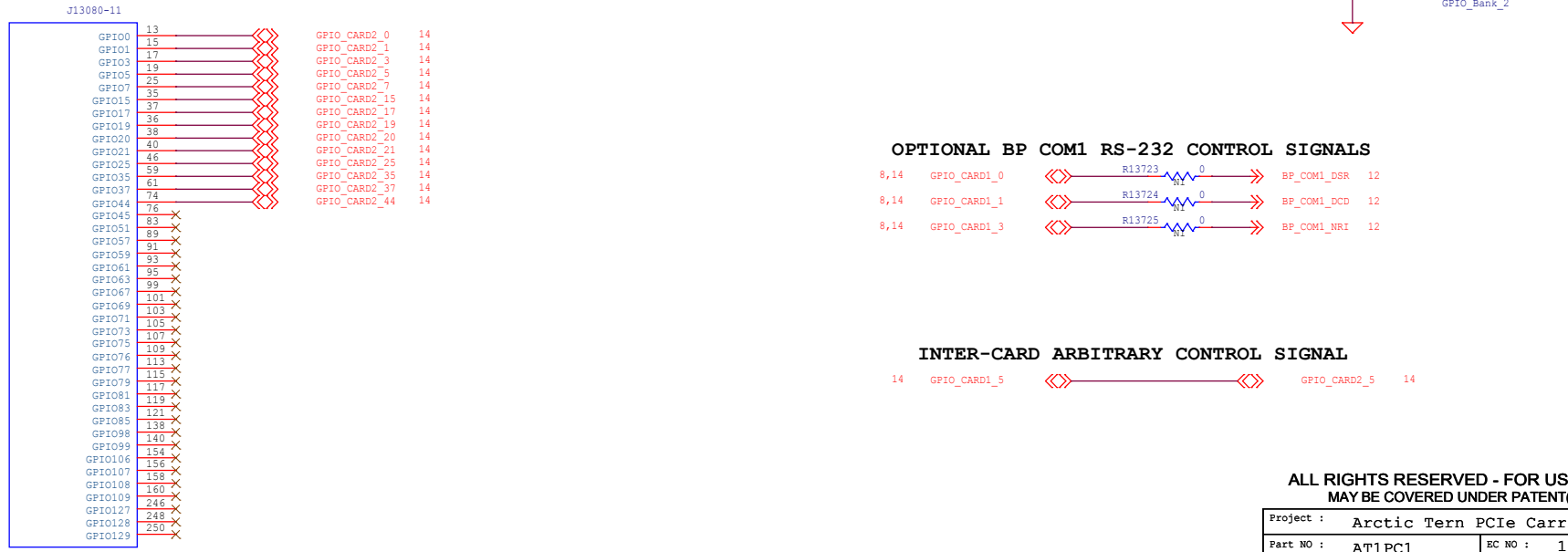
Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	Card 2 COM Ports		
Author :			Page:13 of 27
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GPIO0-GPIO5 ARE NATIVE (RAW) FPGA GPIOS



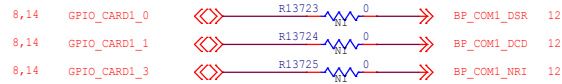
ModBMC-1

GPIO0-GPIO5 ARE NATIVE (RAW) FPGA GPIOS



ModBMC-2


OPTIONAL BP COM1 RS-232 CONTROL SIGNALS

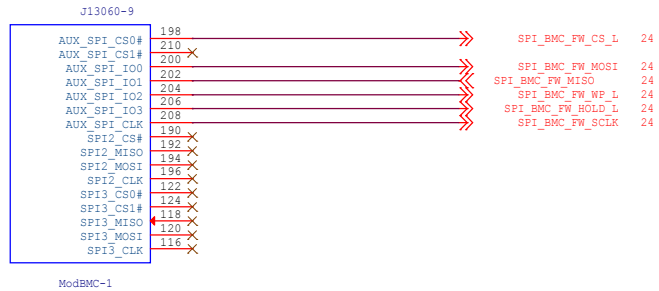
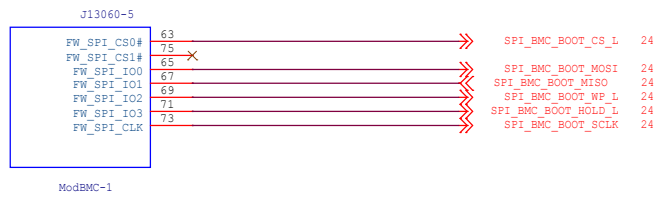


INTER-CARD ARBITRARY CONTROL SIGNAL




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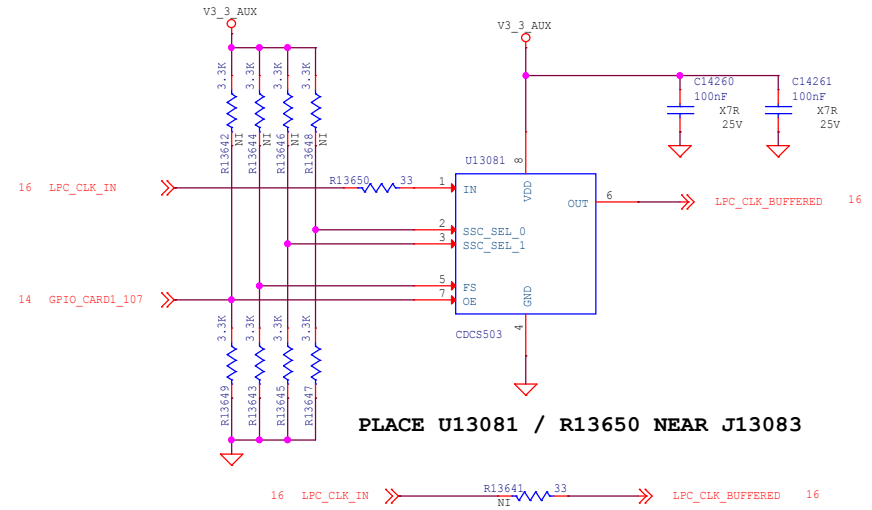
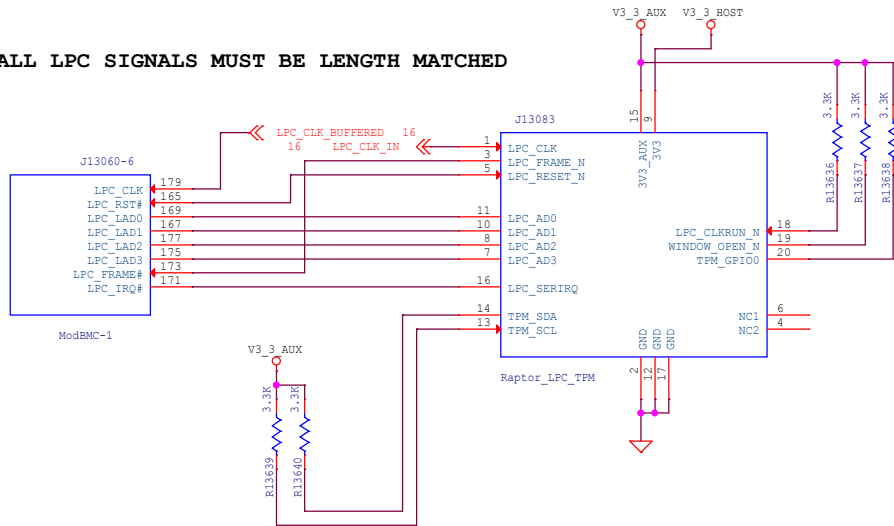
Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	GPIO		
Author :		Page:14	of 27
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Project :	Arctic Tern PCIe Carrier	
Part NO :	AT1PC1	
Title :	Module SPI	
Author :		Page:15 of 27
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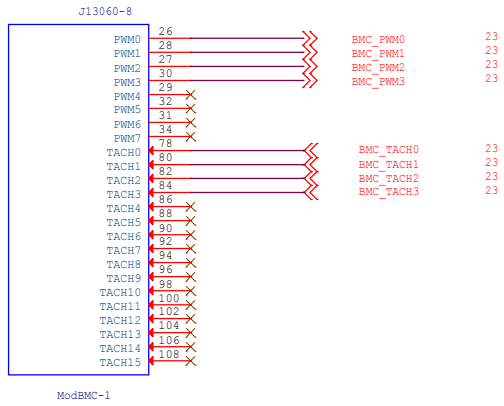
ALL LPC SIGNALS MUST BE LENGTH MATCHED




PLACE U13081 / R13650 NEAR J13083

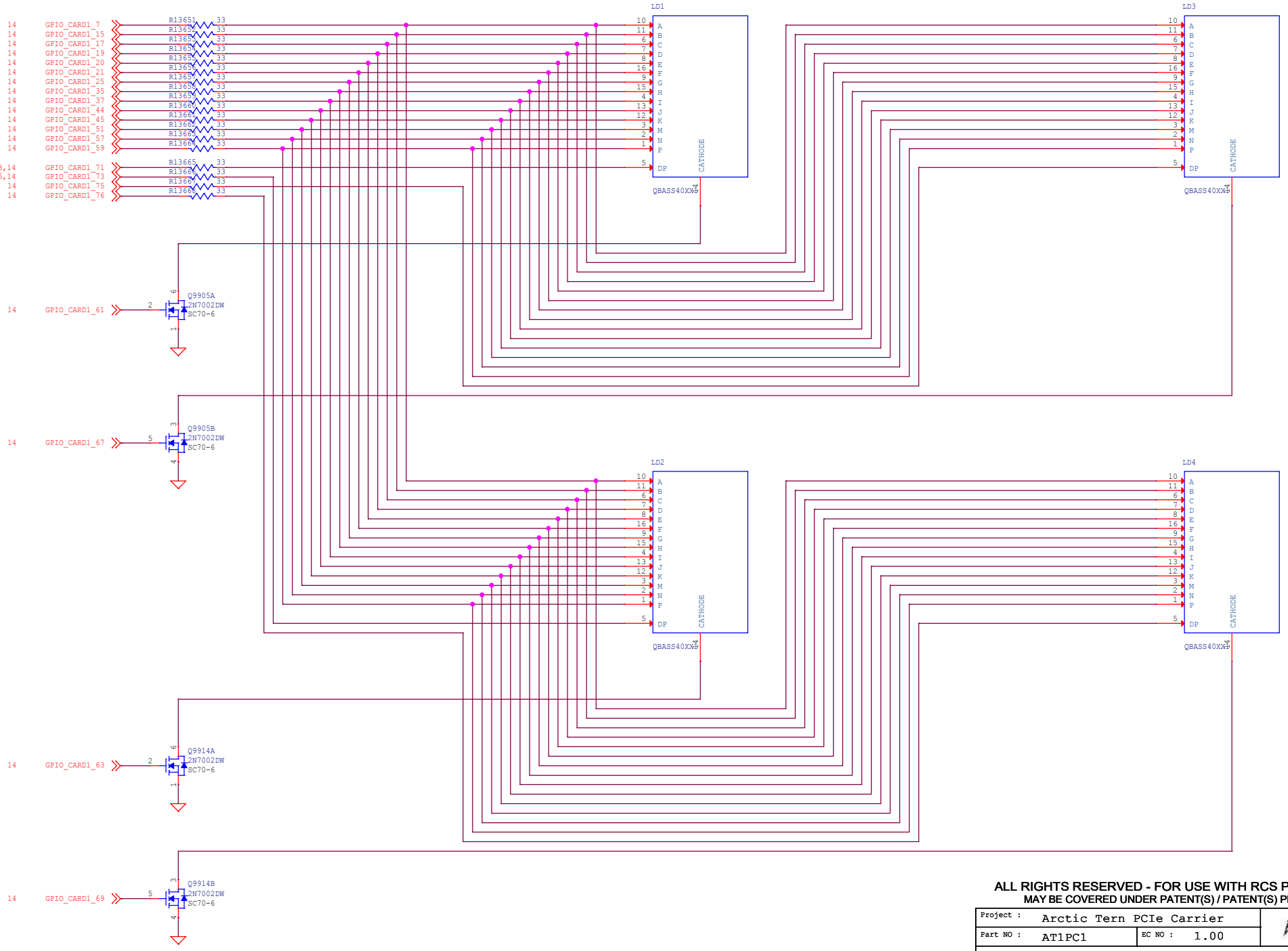
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Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	Module LPC		
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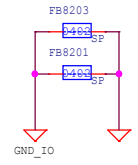
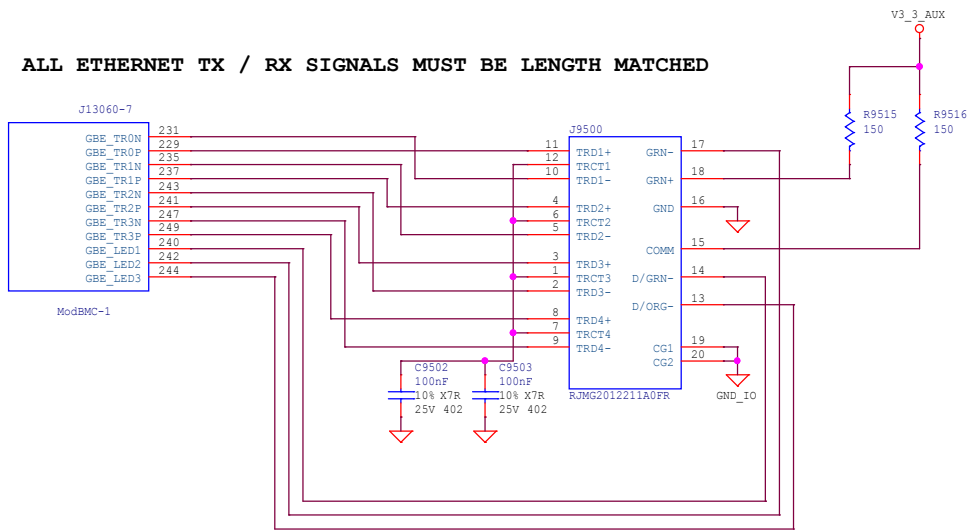
Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : Module Fan / Tach		
Author :	Page:17 of 27	
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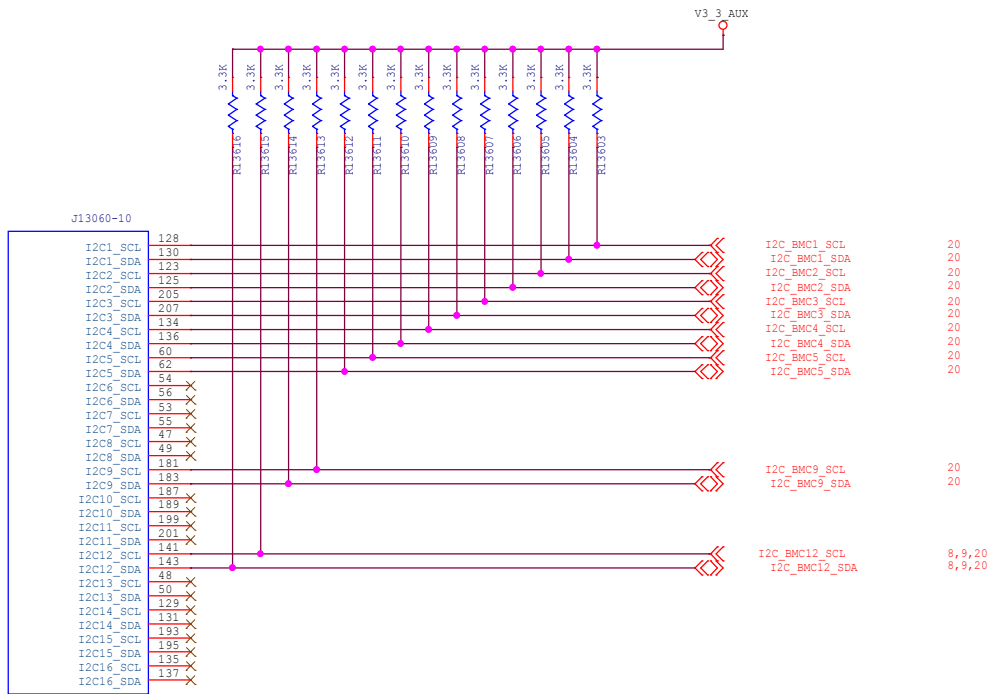
Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : LED Display		
Author :	Page:18 of 27	
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ALL ETHERNET TX / RX SIGNALS MUST BE LENGTH MATCHED



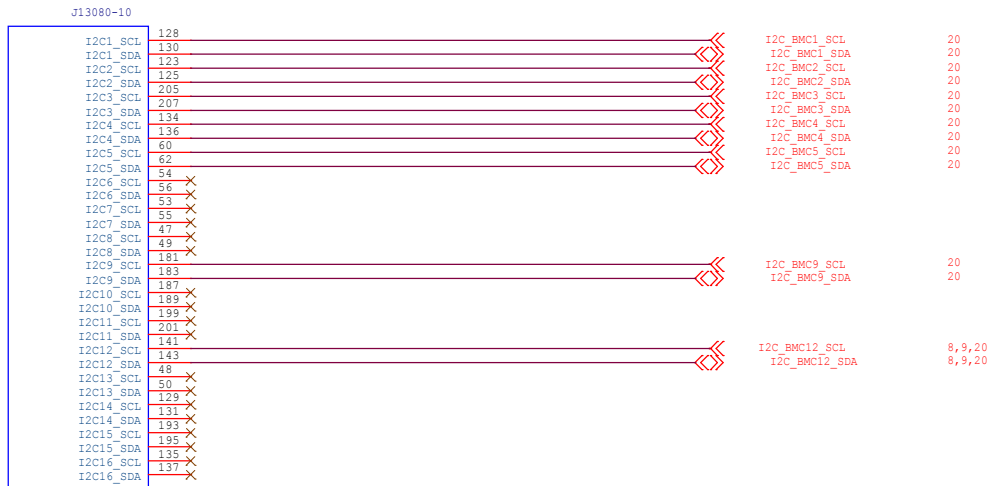
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Project :	Arctic Tern PCIe Carrier		
Part NO :	AT1PC1	EC NO : 1.00	
Title :	Ethernet		
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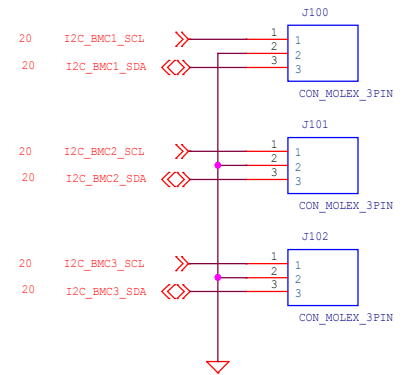
J13060-10

ModBMC-1




J13080-10

ModBMC-2



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Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : Module I2C		
Author :	Page:20 of 27	
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Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : Blank		
Author :		Page:21 of 27
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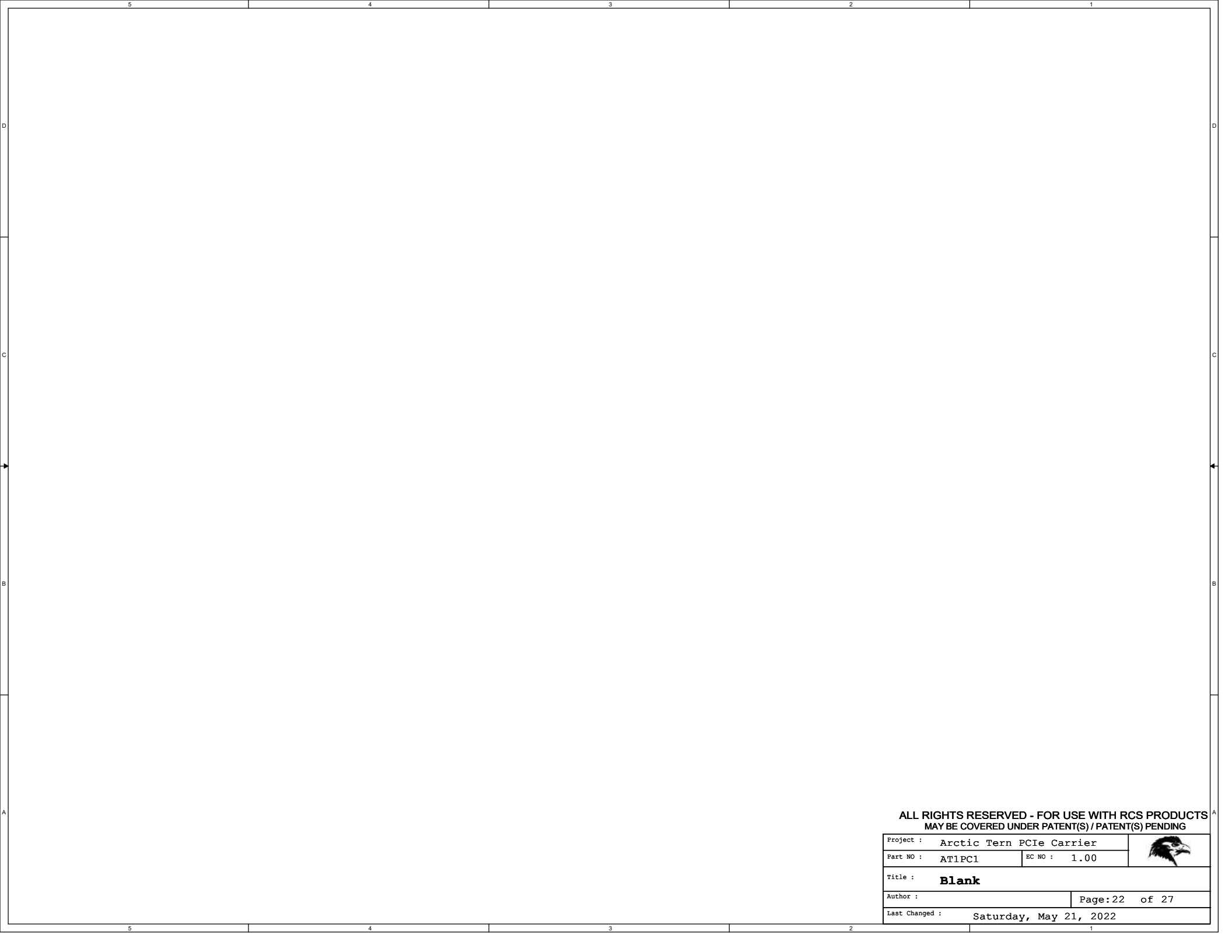
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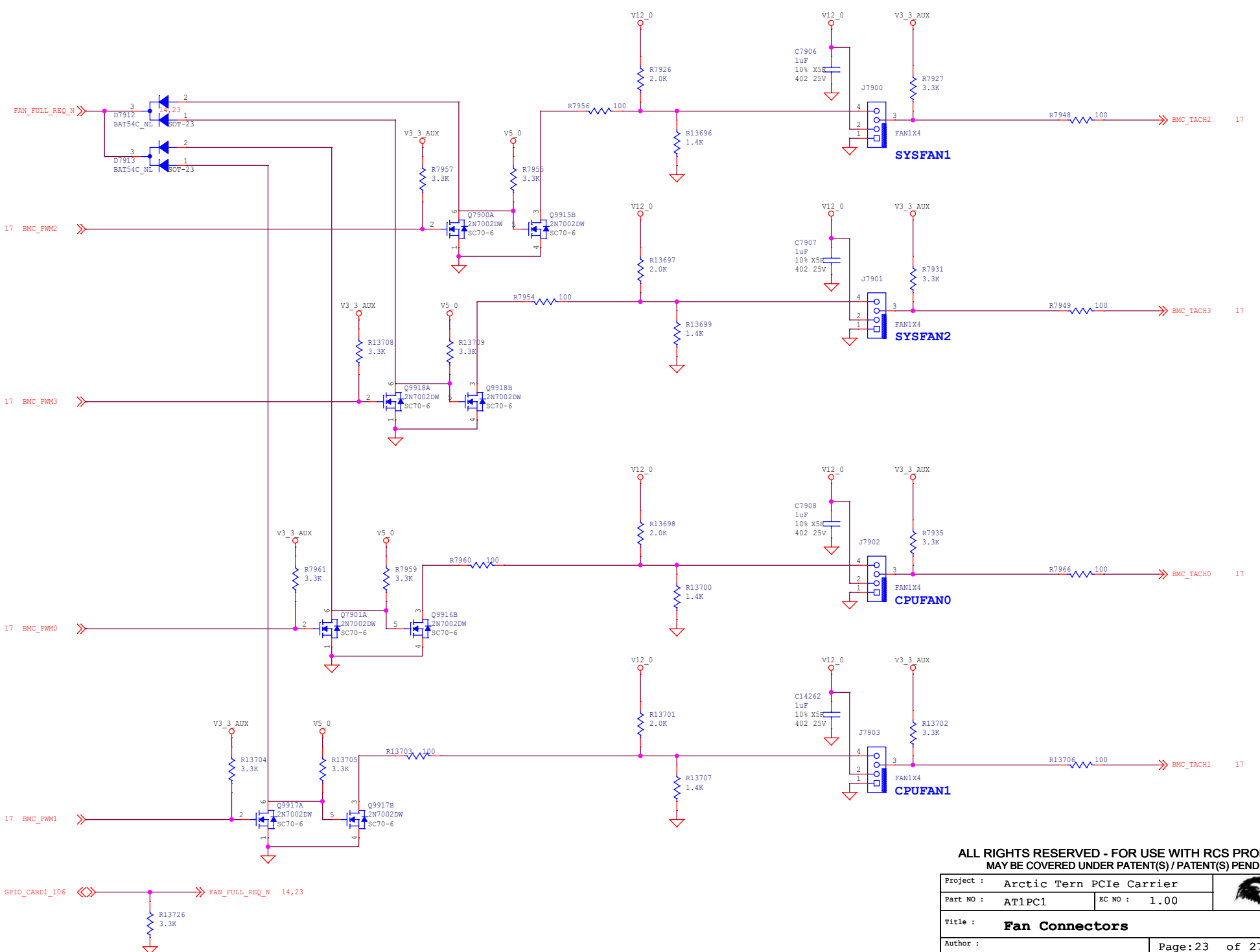
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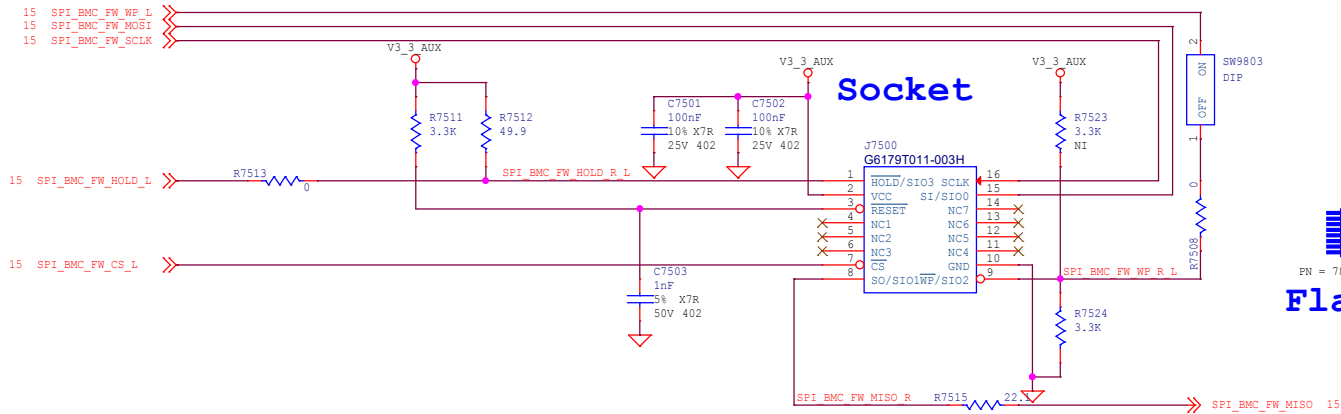
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Part NO :	AT1PC1	
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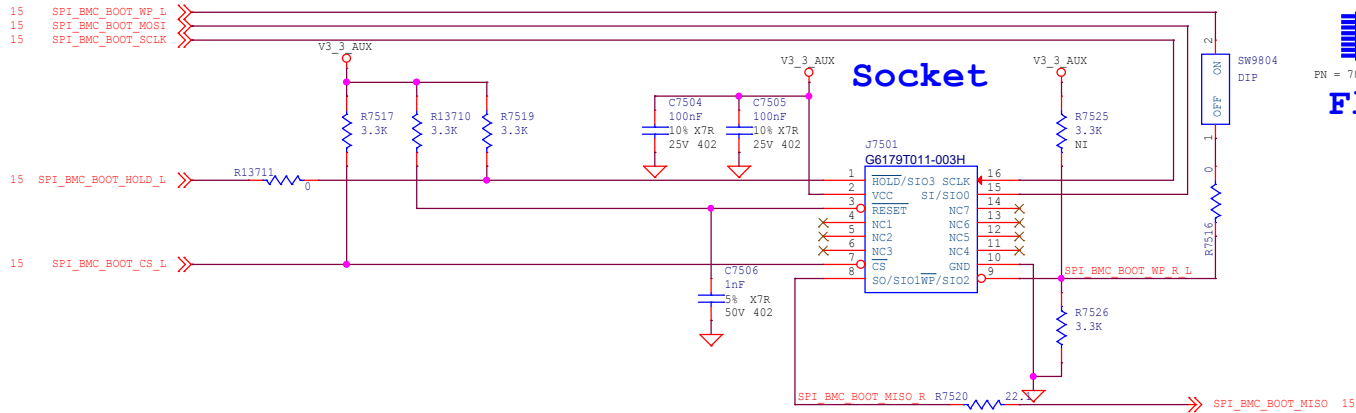


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Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : Fan Connectors		
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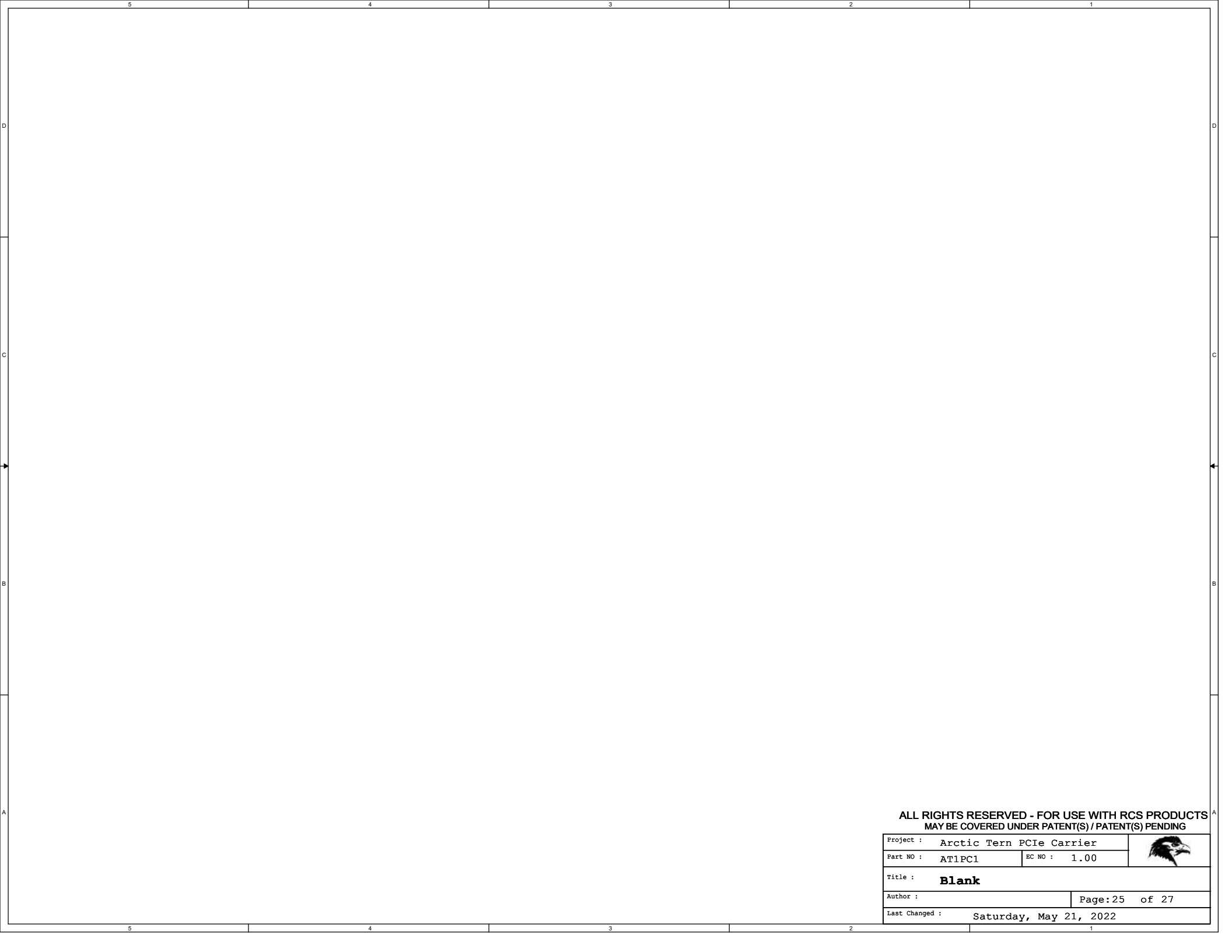
BMC Firmware ROM




PNOR Flash ROM

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Project :	Arctic Tern PCIe Carrier	
Part NO :	AT1PC1	
Title :	Firmware ROMs	
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Part NO :	AT1PC1	
Title :	Blank	
Author :		Page:25 of 27
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Project : Arctic Tern PCIe Carrier		
Part NO : AT1PC1	EC NO : 1.00	
Title : Blank		
Author :		Page:26 of 27
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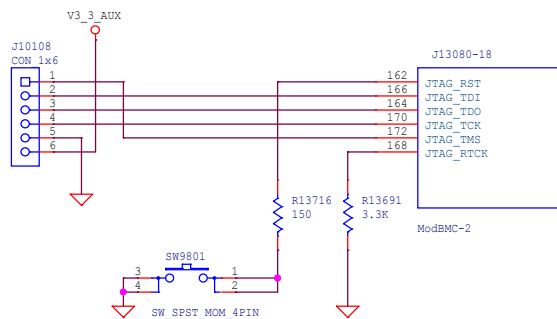
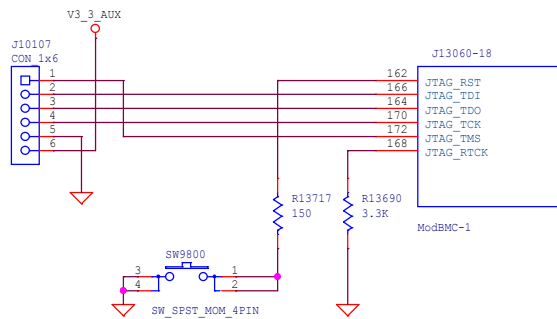
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Project :	Arctic Tern PCIe Carrier	
Part NO :	AT1PC1	
Title :	Programming Headers	
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